

208110-19805007

FIG.1 PRIOR ART

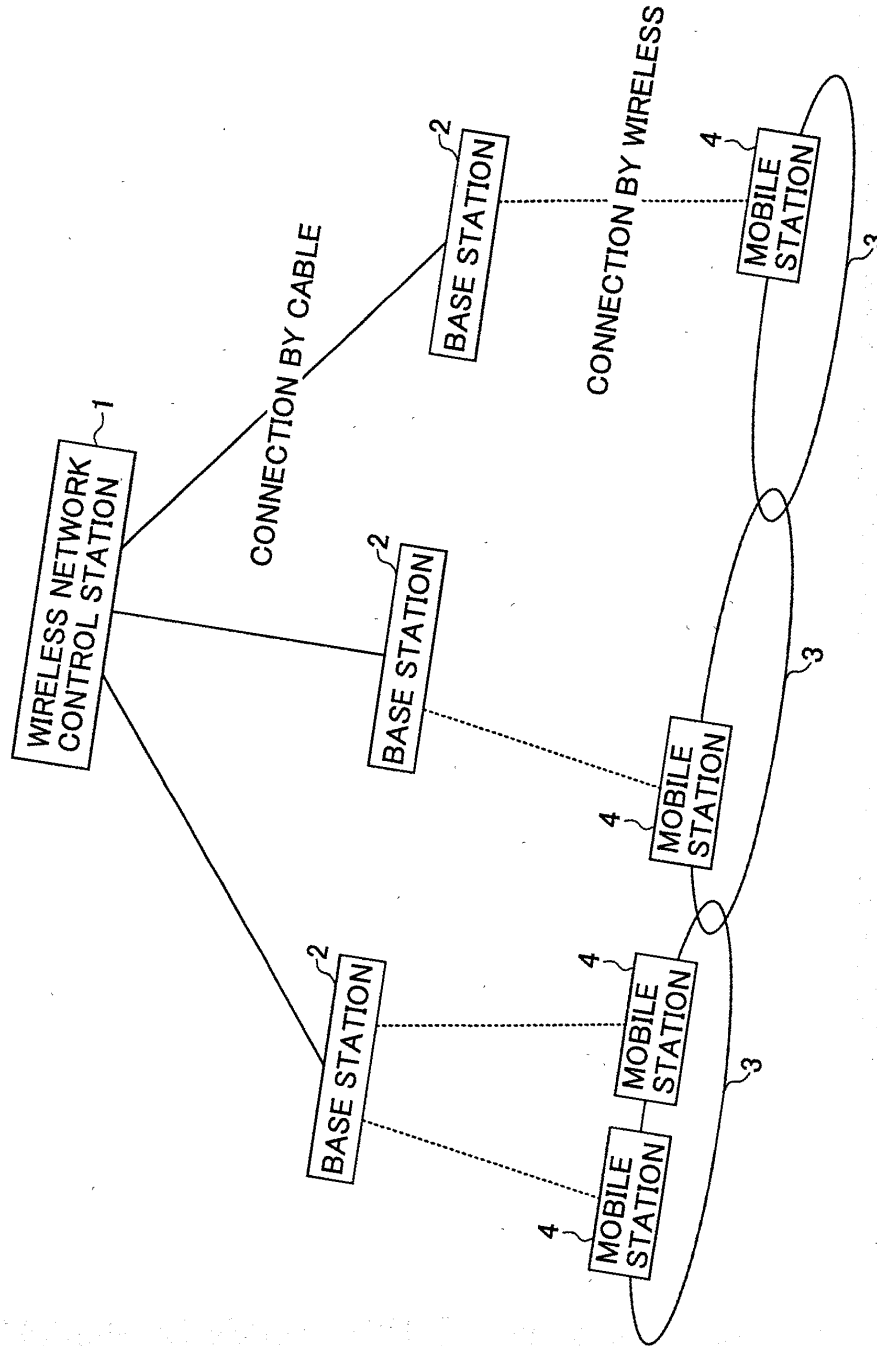
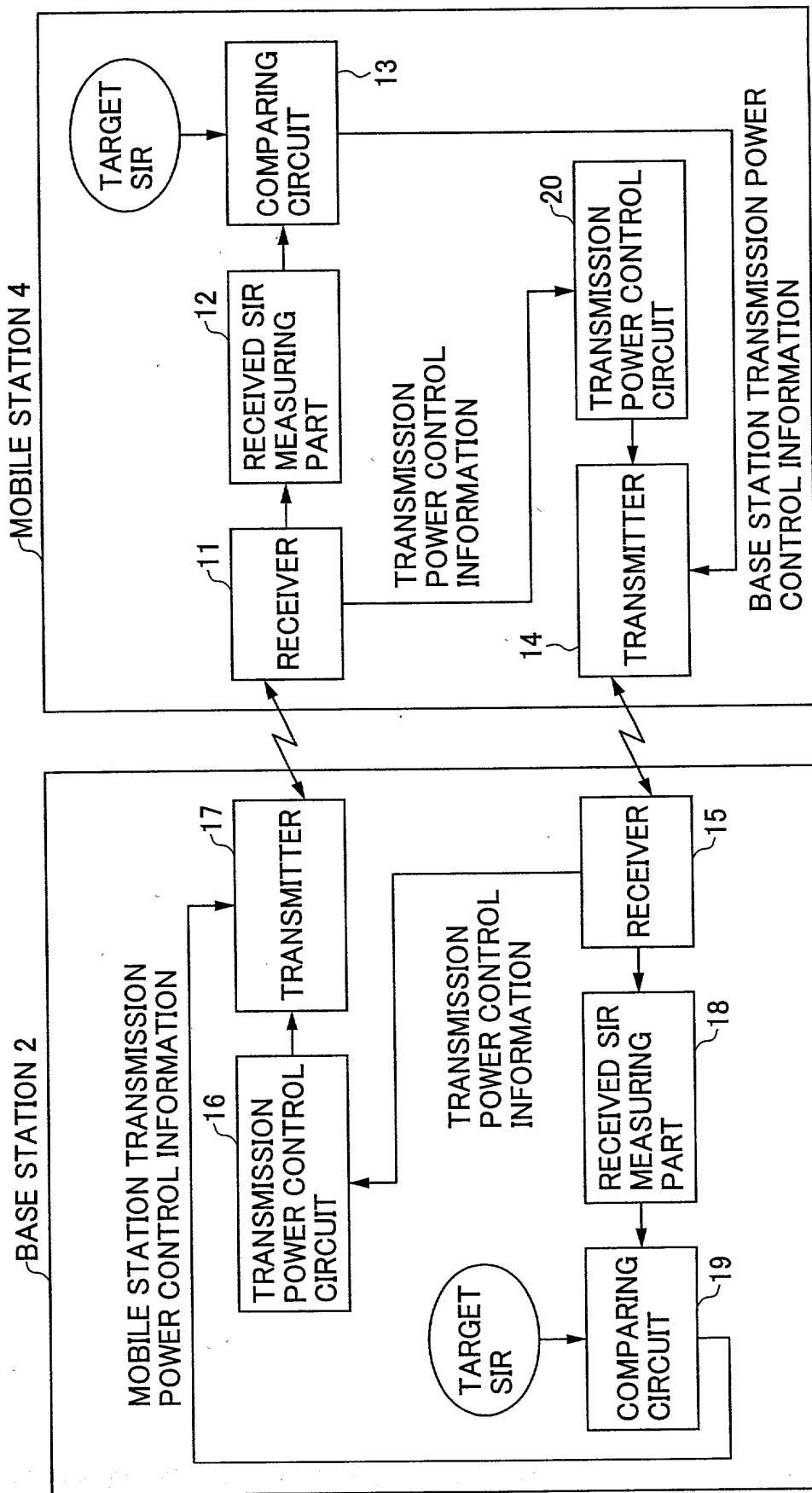


FIG.2 PRIOR ART



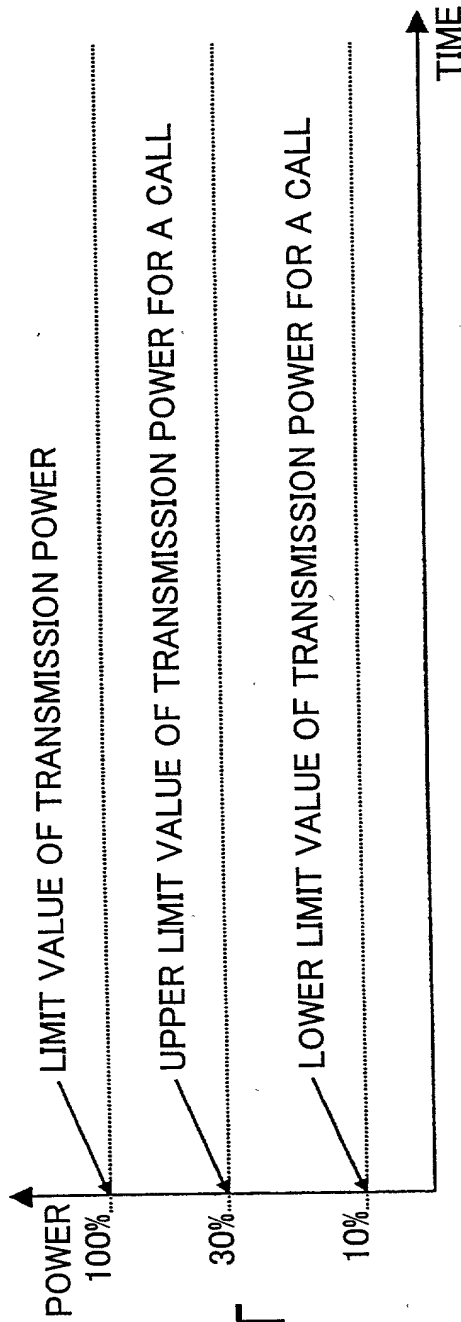


FIG.3A
PRIOR ART

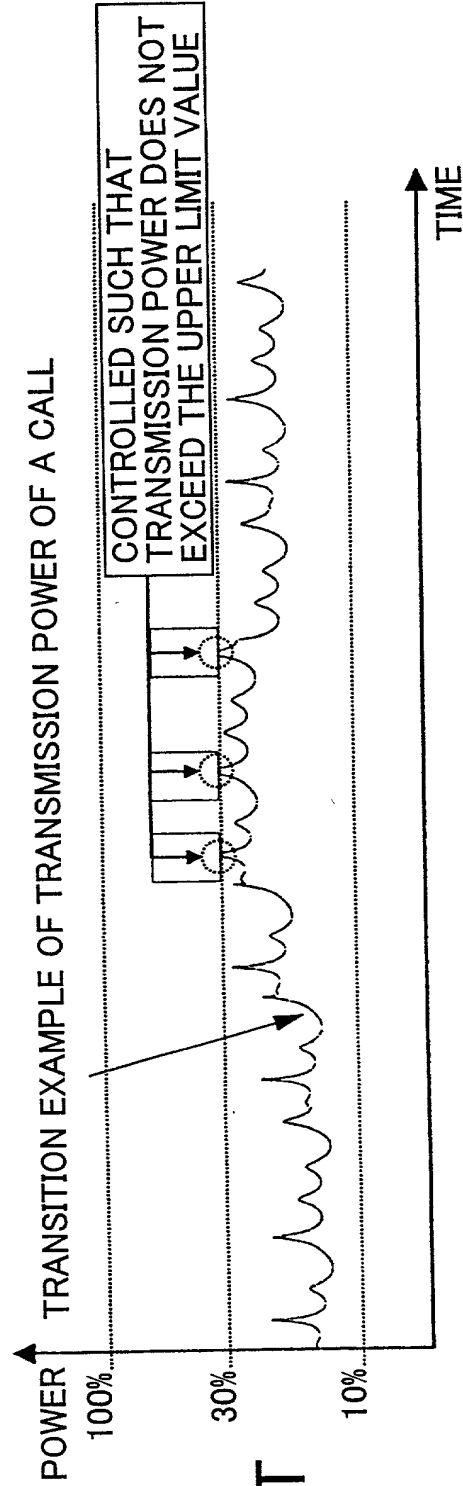


FIG.3B
PRIOR ART

FIG. 4A PRIOR ART

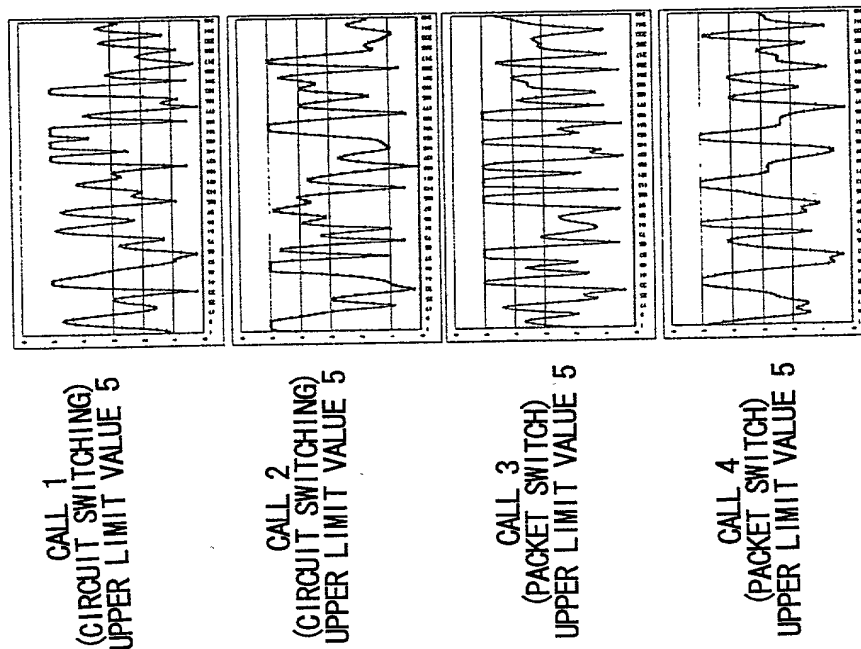


FIG. 4B PRIOR ART

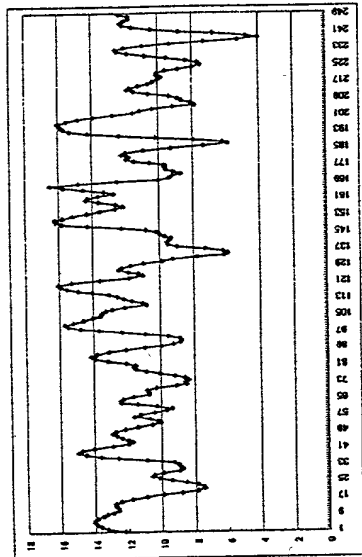


FIG. 4C PRIOR ART

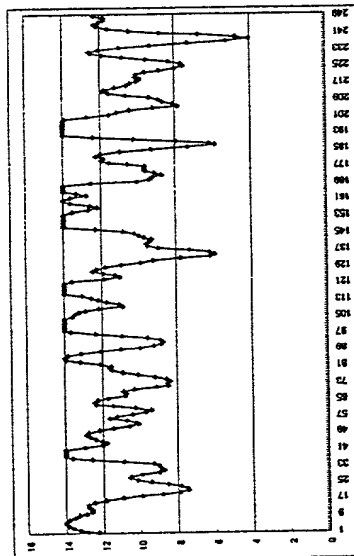
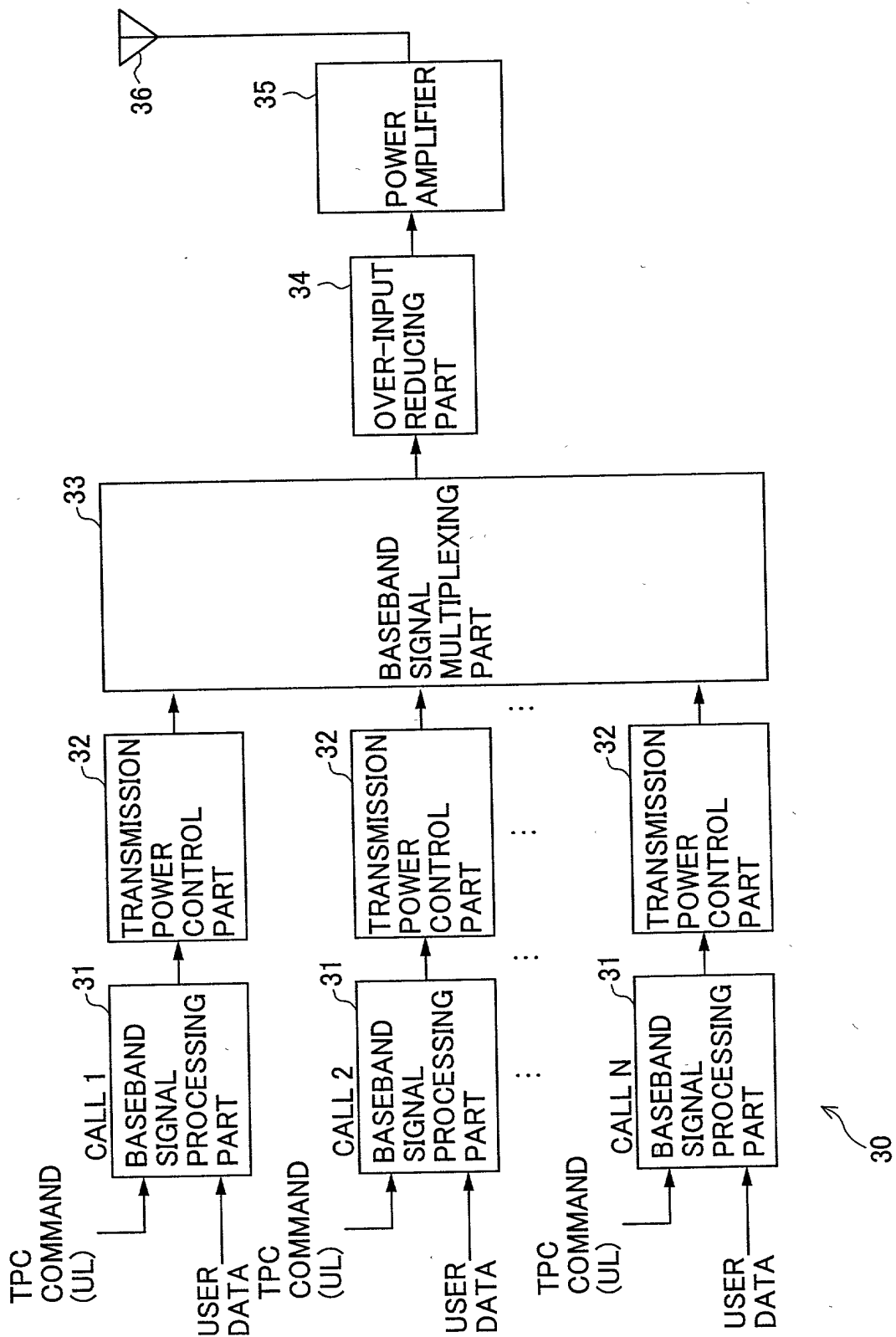
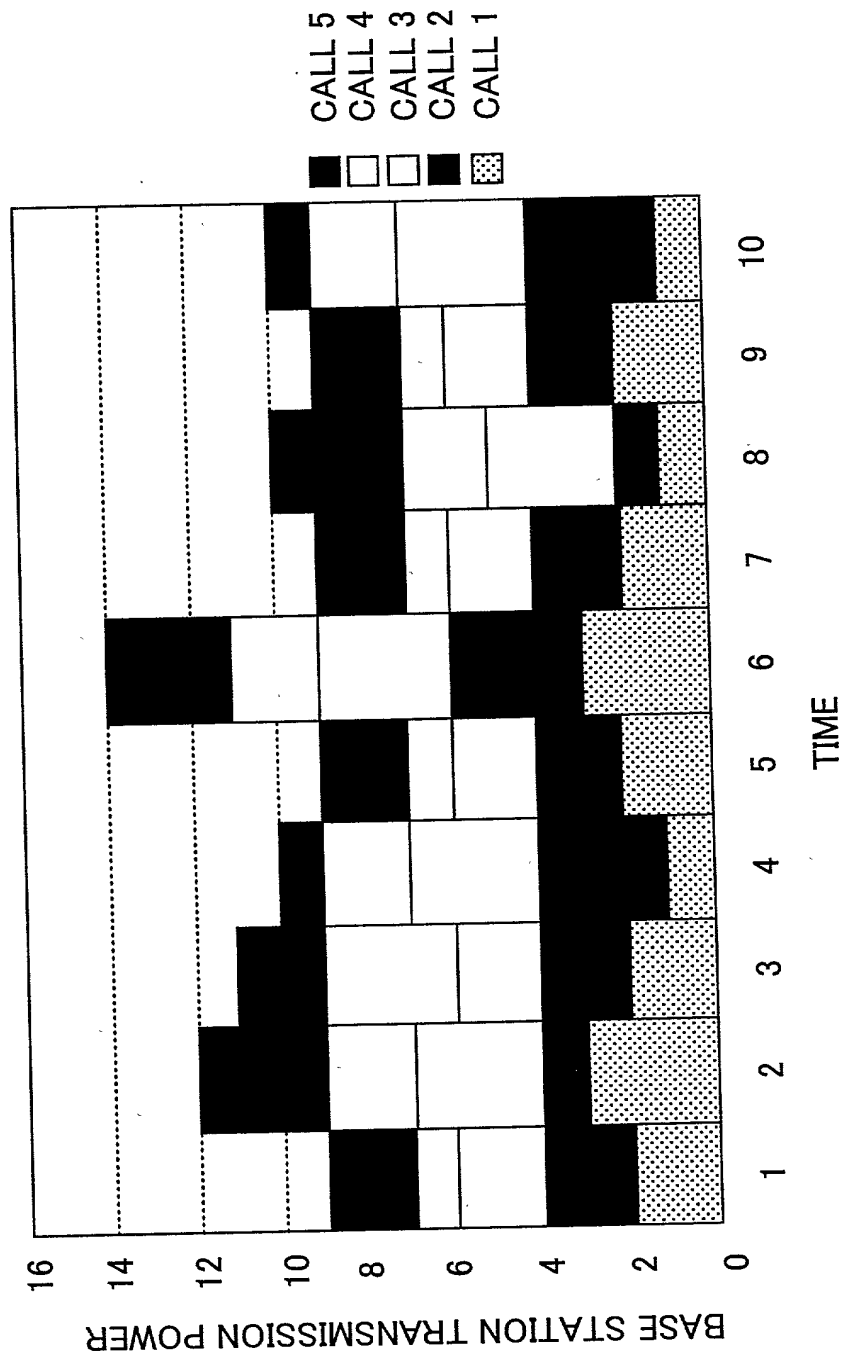


FIG.5 PRIOR ART



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FIG.6 PRIOR ART



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FIG.7 PRIOR ART

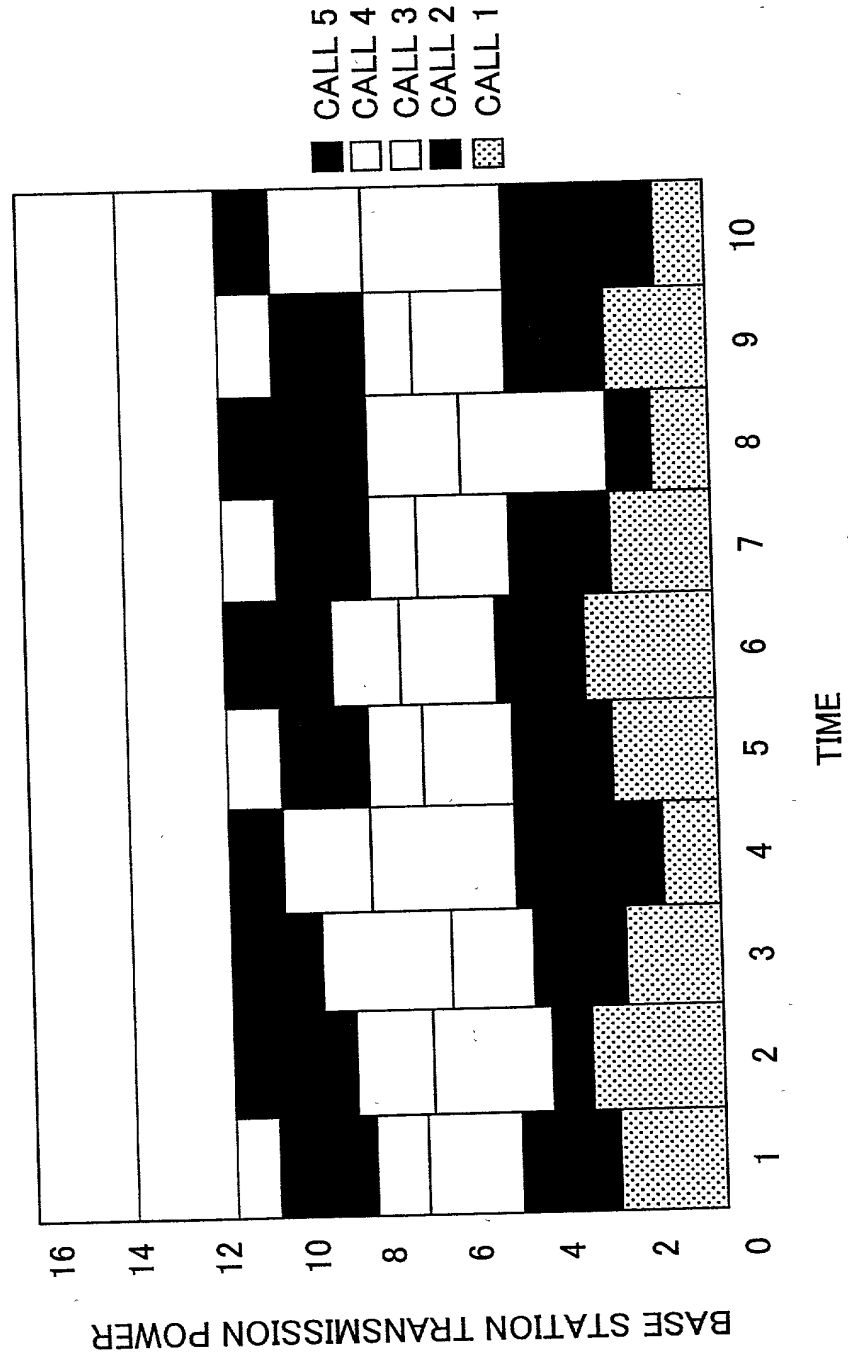


FIG.8

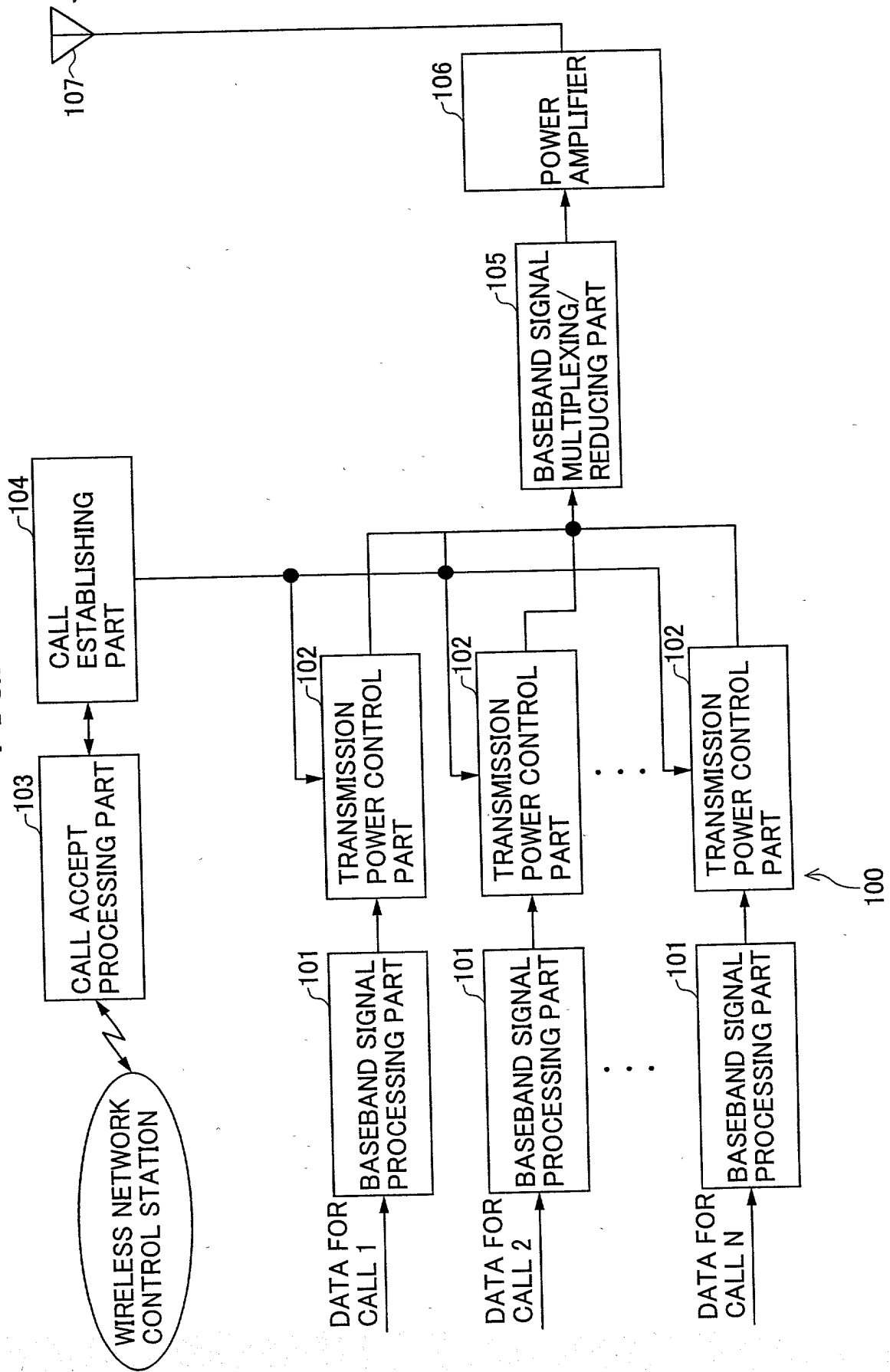
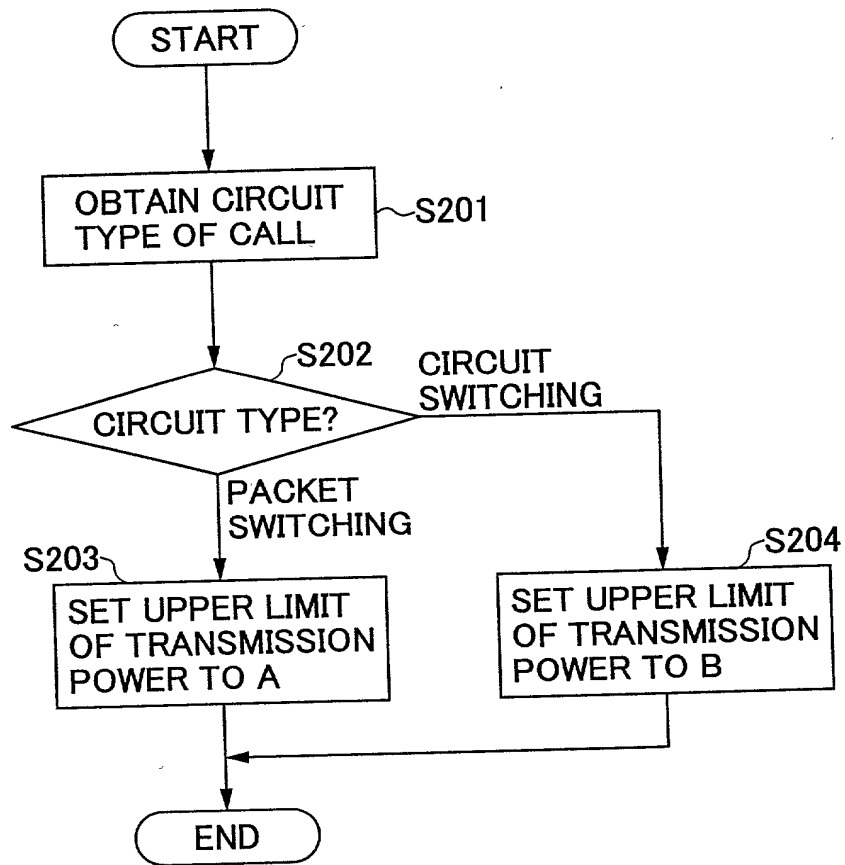


FIG.9



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FIG. 10B

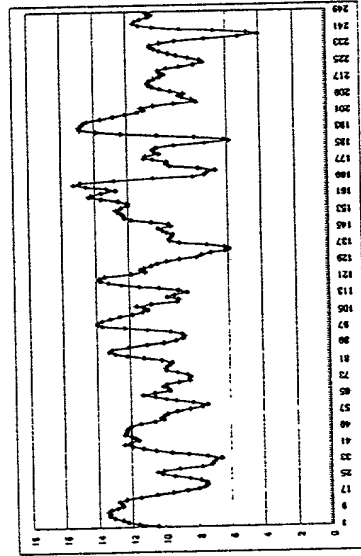


FIG. 10C

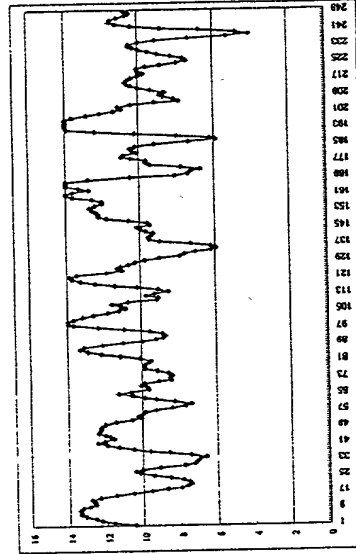
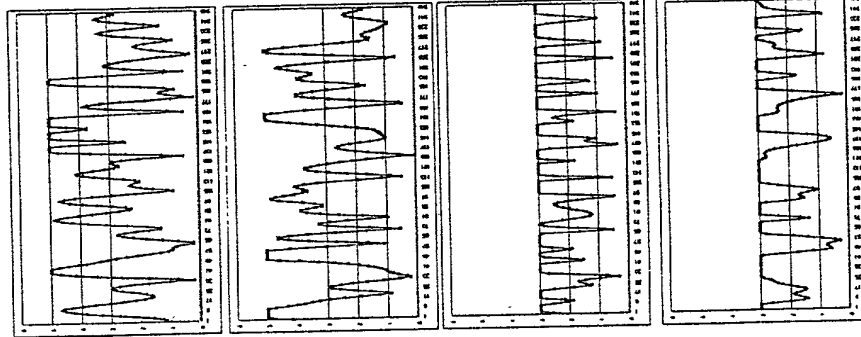


FIG. 10A



CALL 1
(CIRCUIT SWITCHING)
UPPER LIMIT VALUE 5

CALL 2
(CIRCUIT SWITCHING)
UPPER LIMIT VALUE 5

CALL 3
(PACKET SWITCH)
UPPER LIMIT VALUE 3

CALL 4
(PACKET SWITCH)
UPPER LIMIT VALUE 3

FIG.11

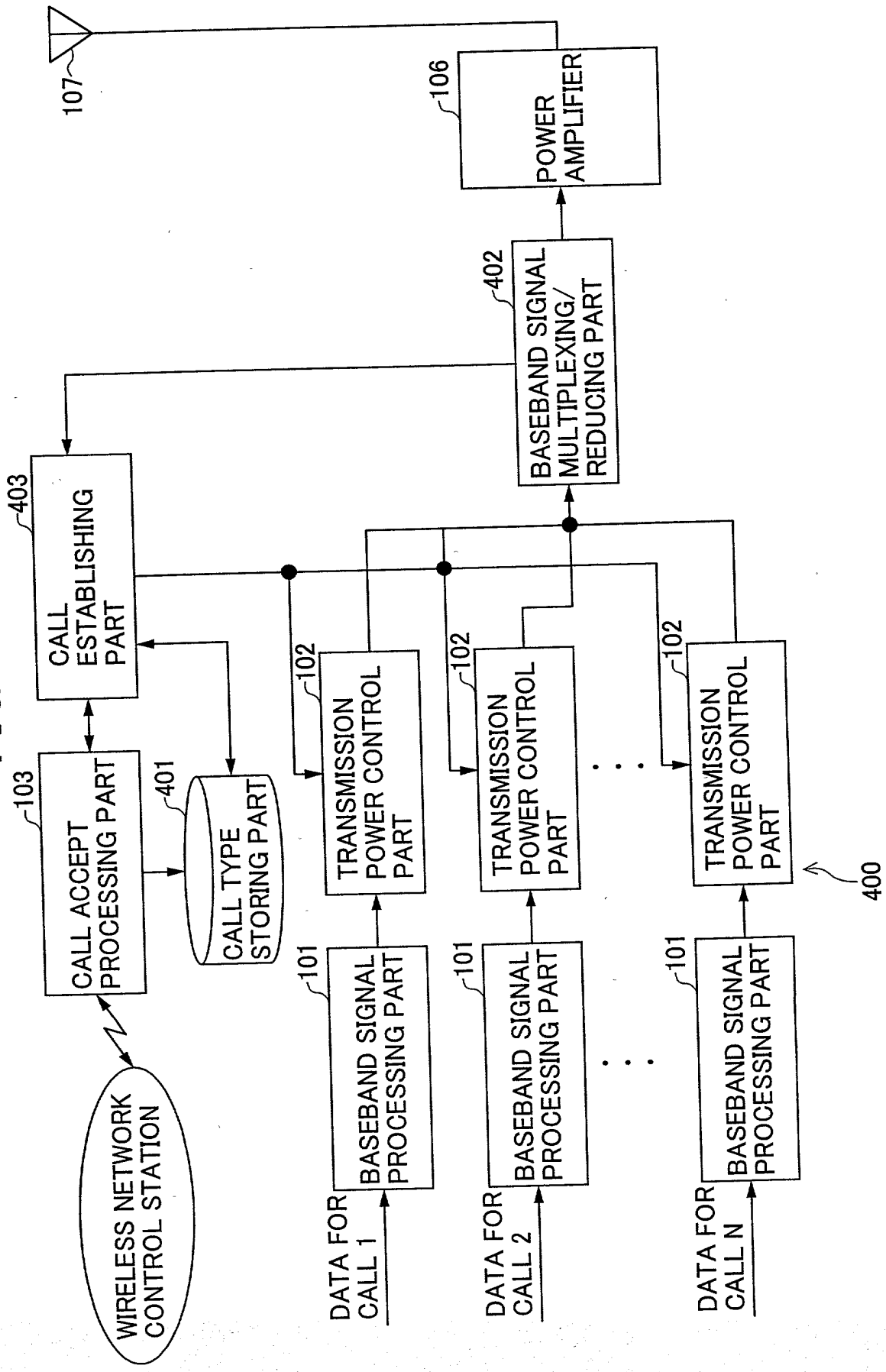
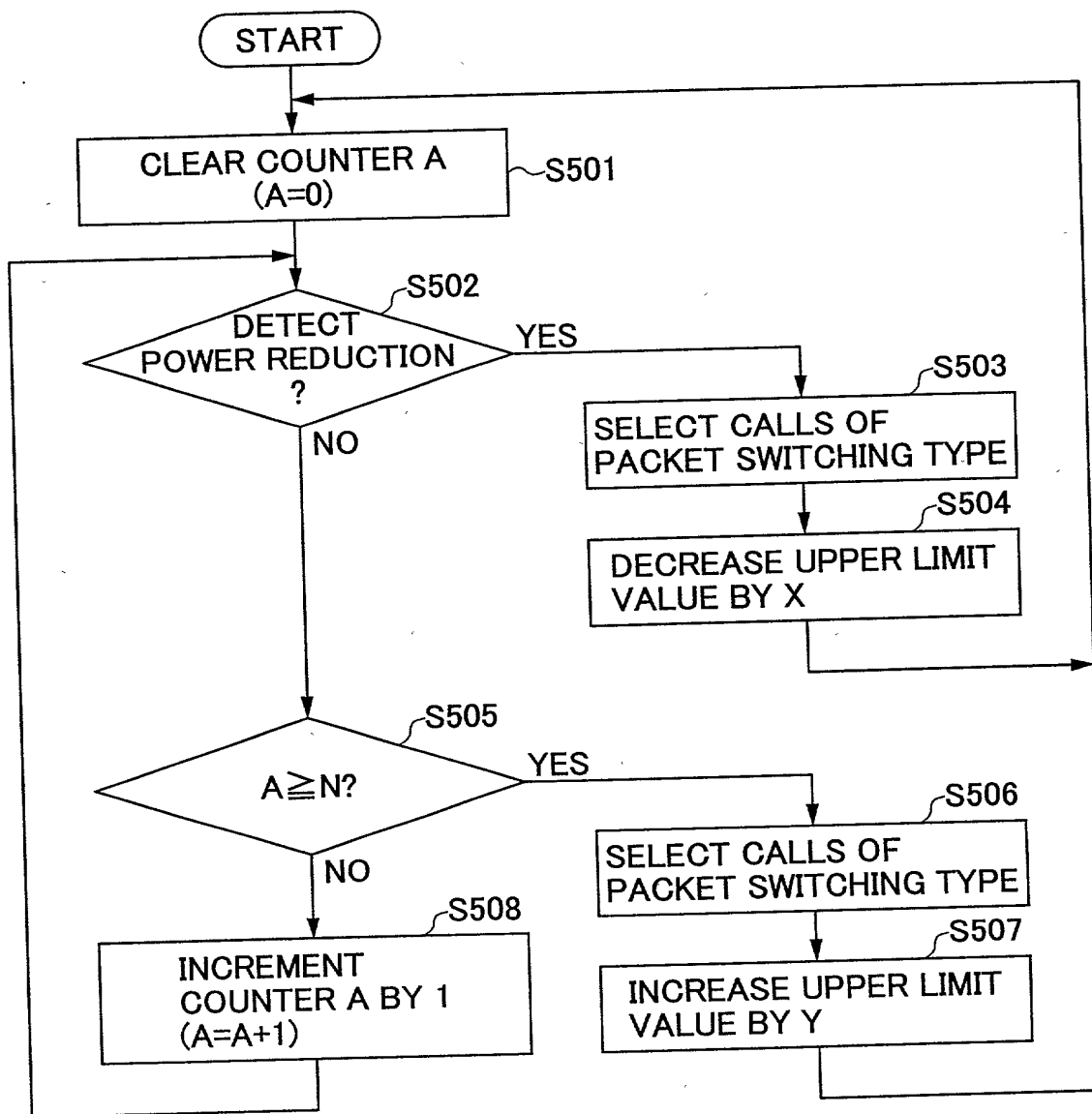


FIG.12



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FIG.13

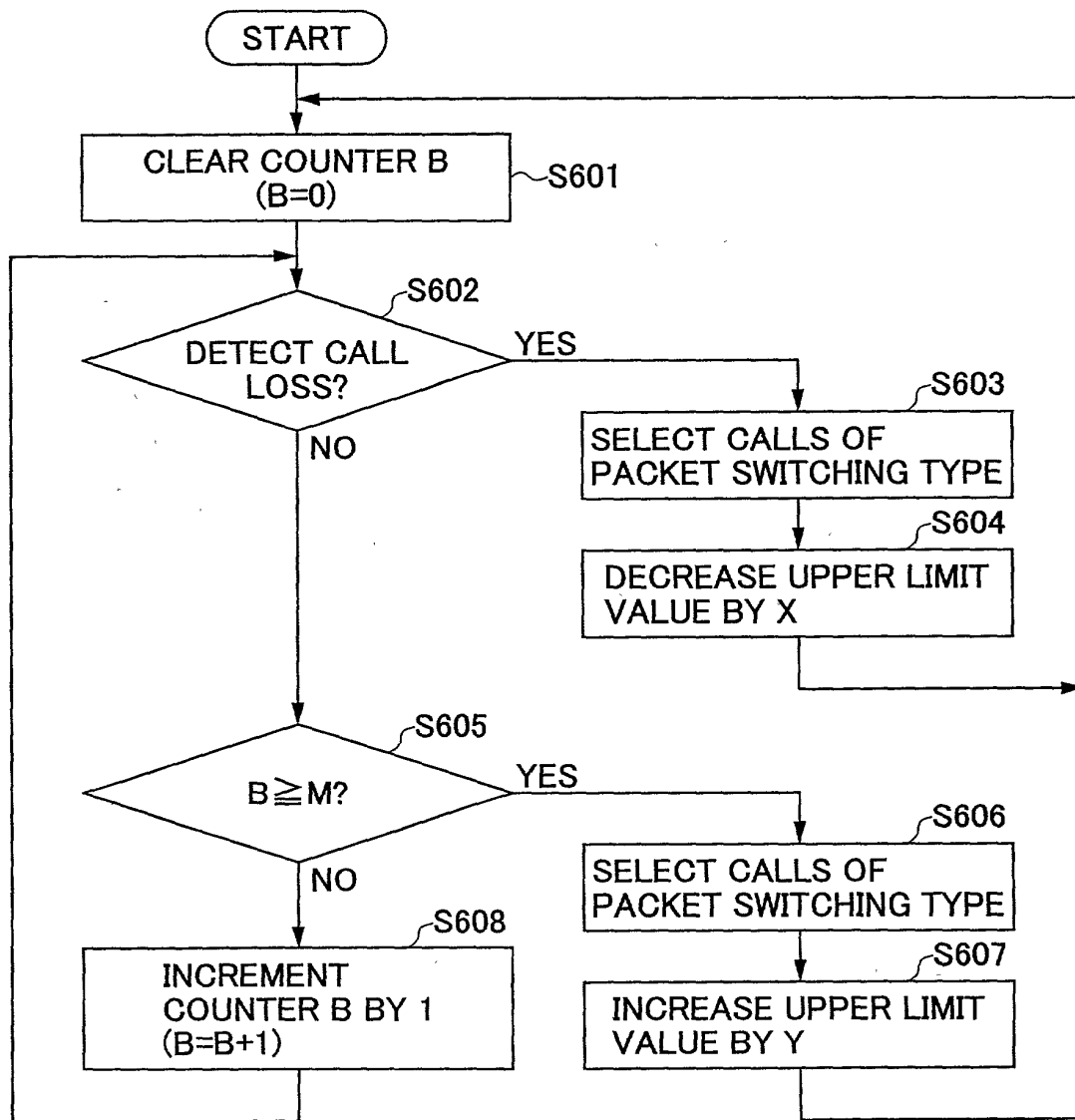


FIG.14

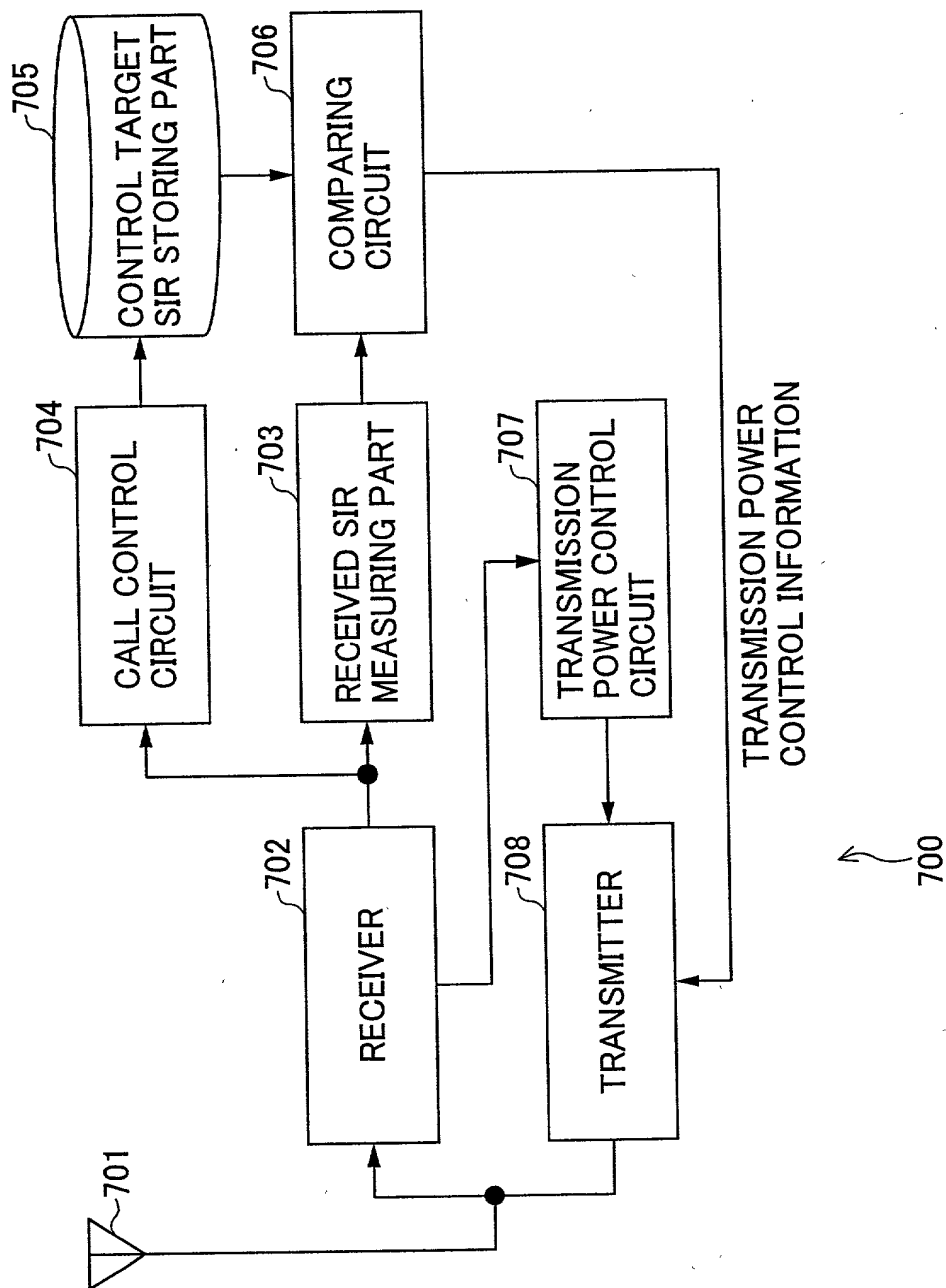
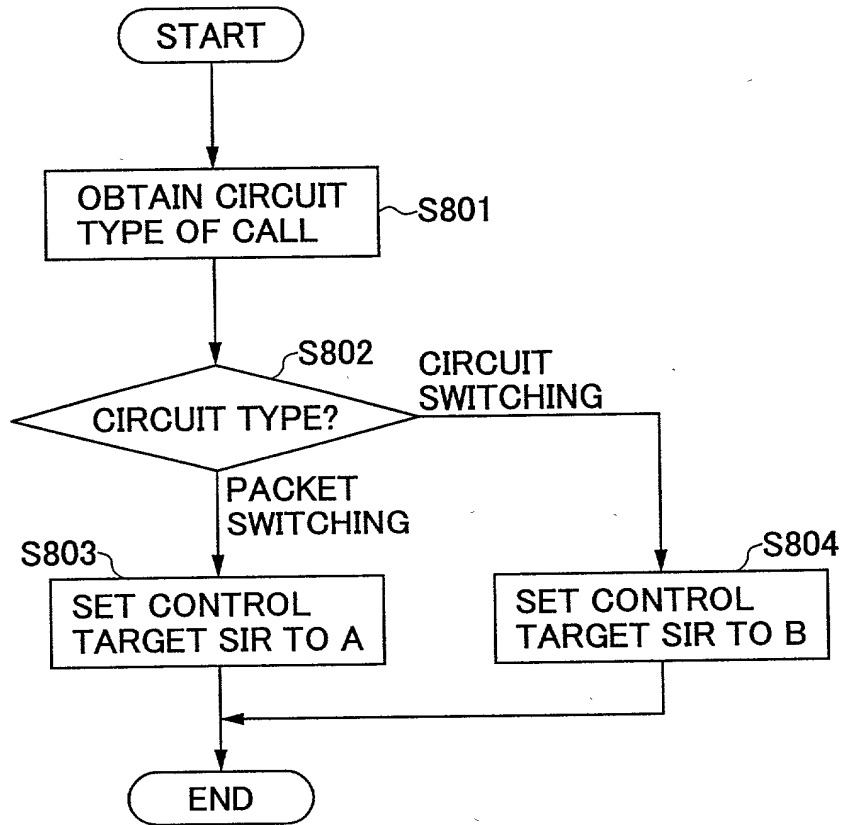


FIG.15



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FIG.16

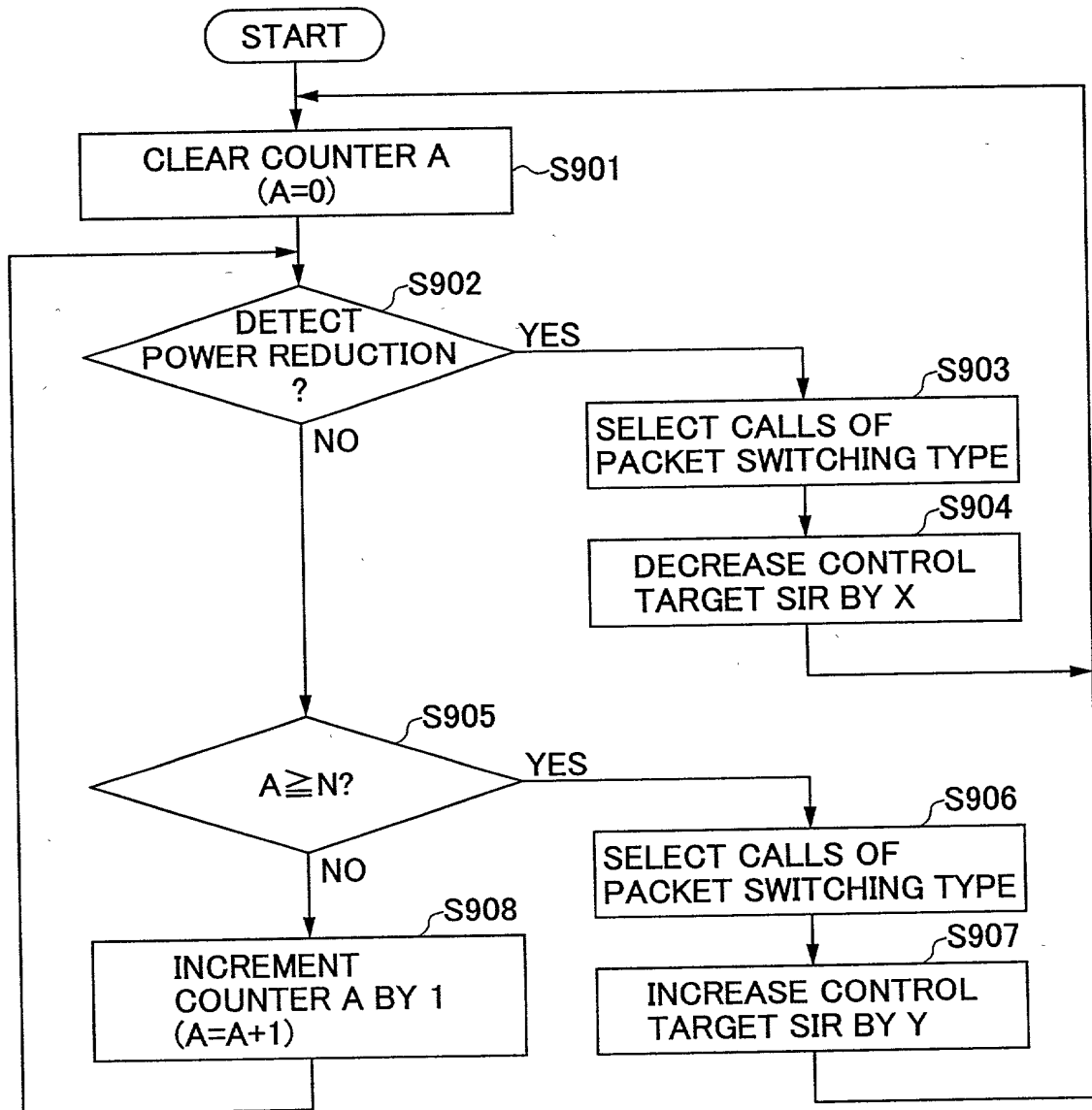


FIG.17

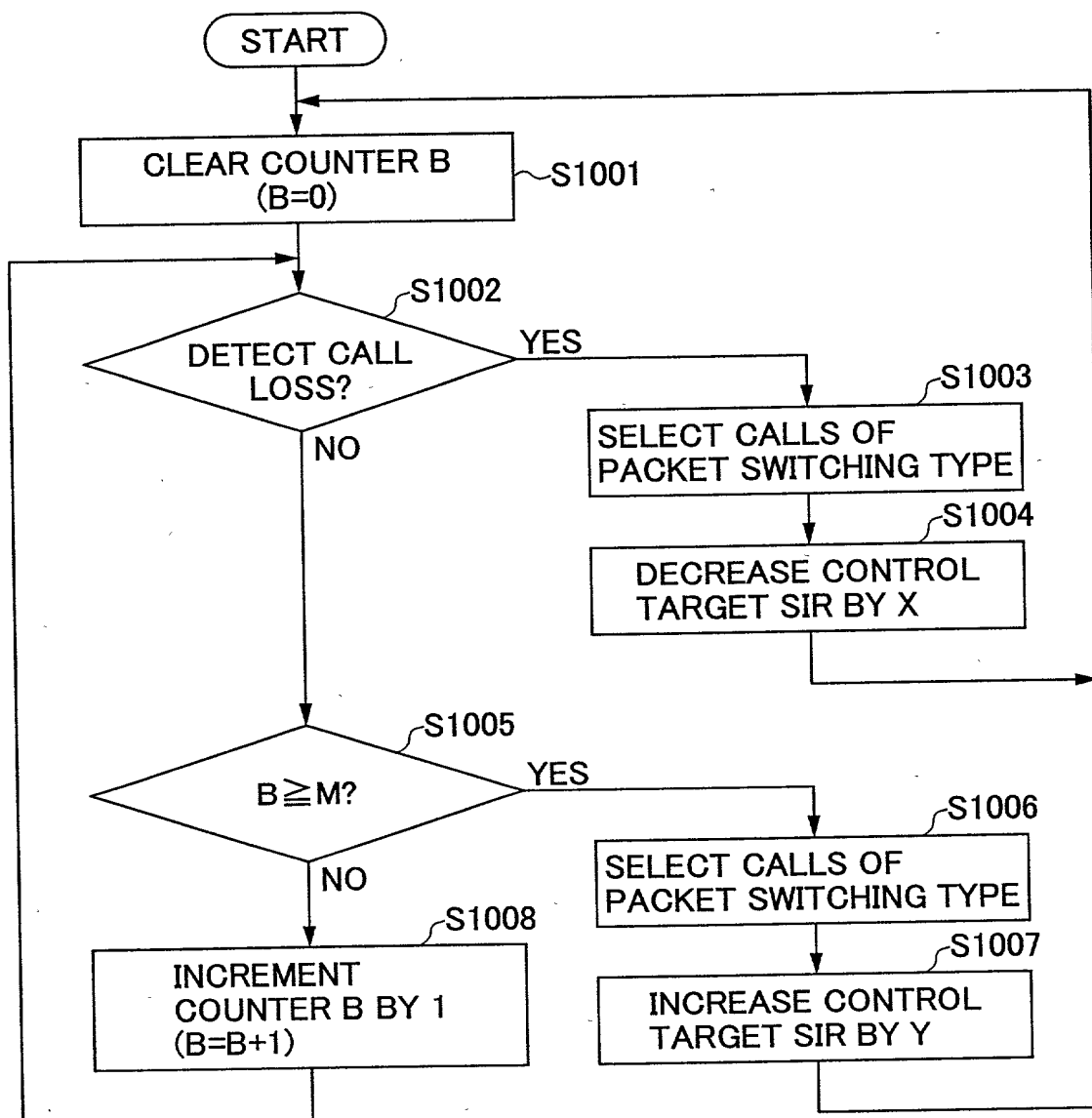


FIG.18

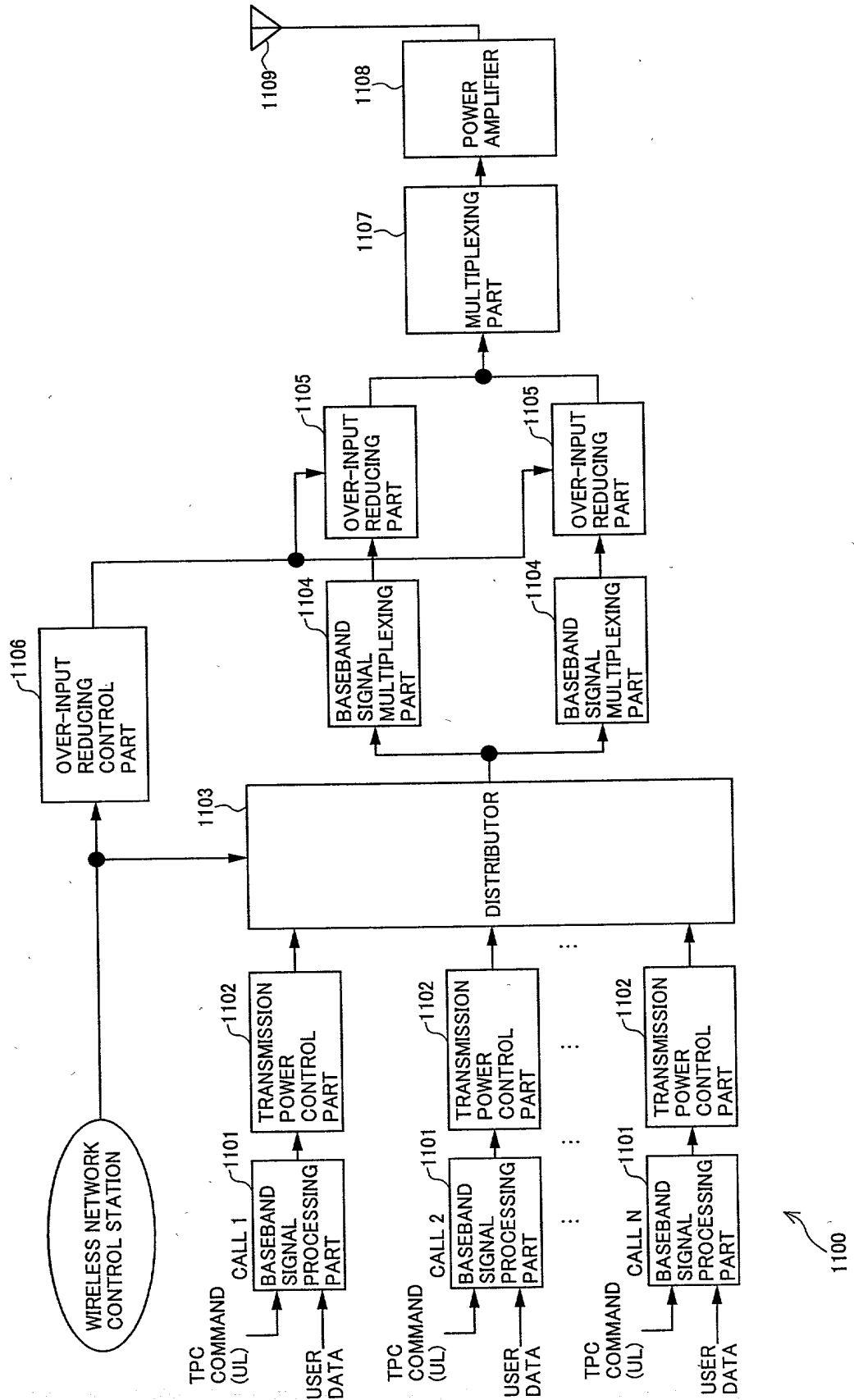


FIG. 19

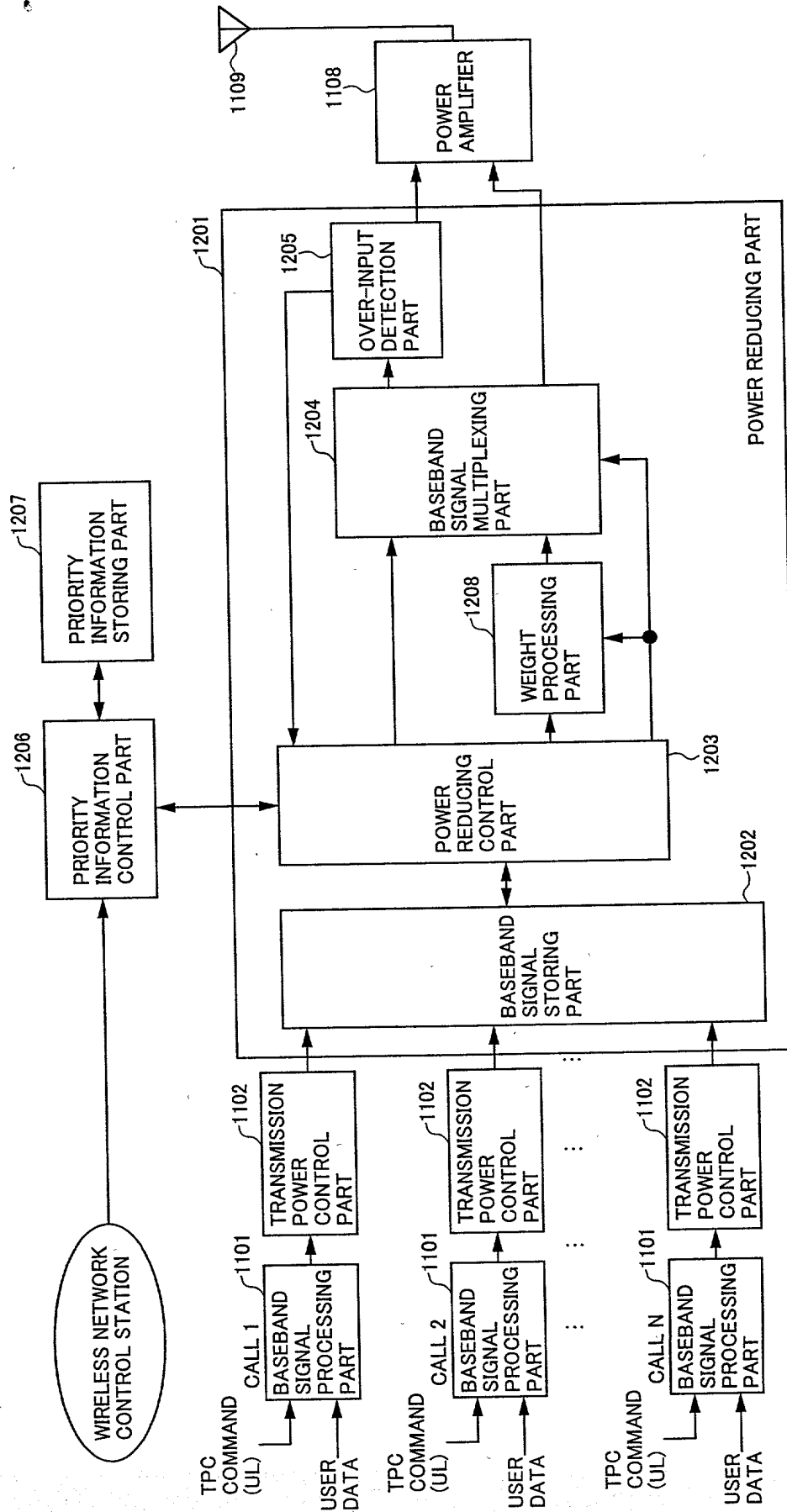


FIG.20

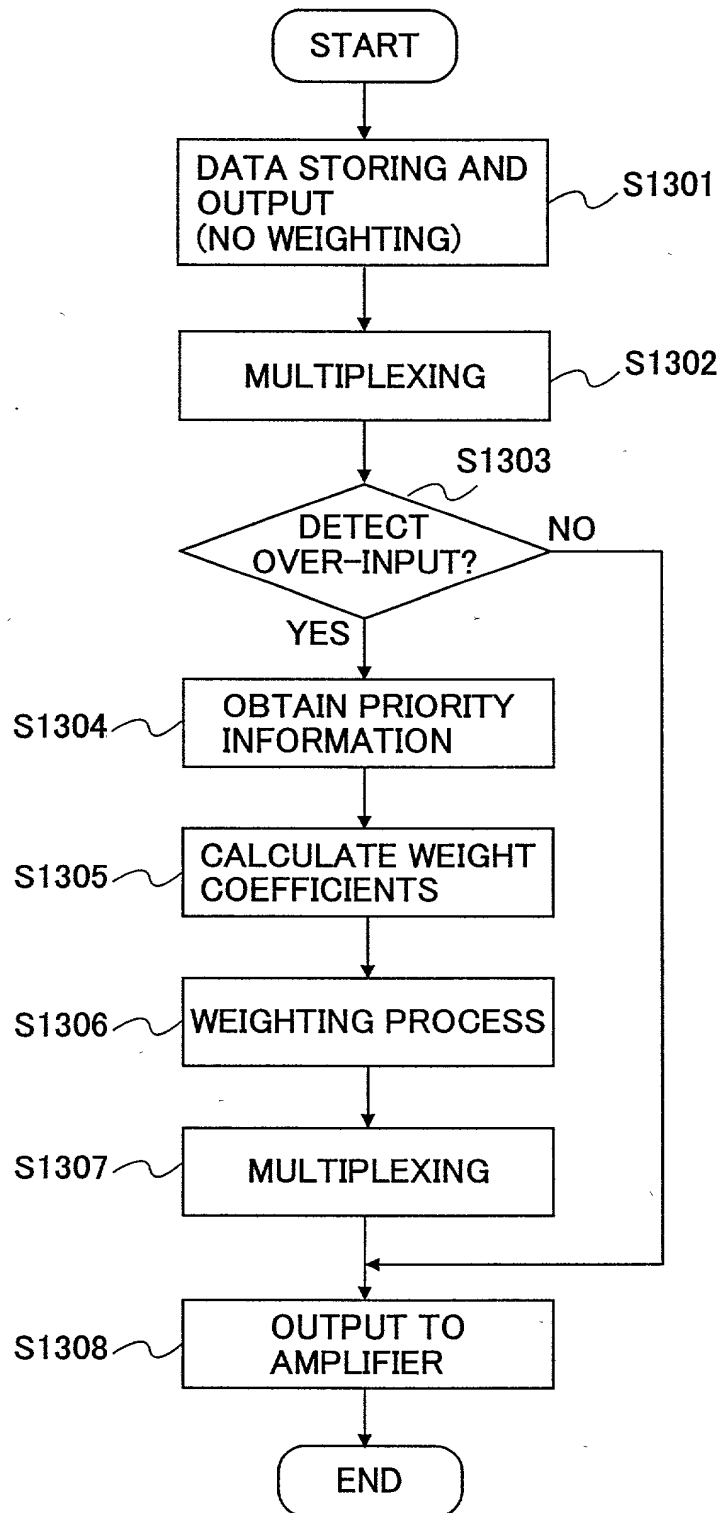


FIG.21

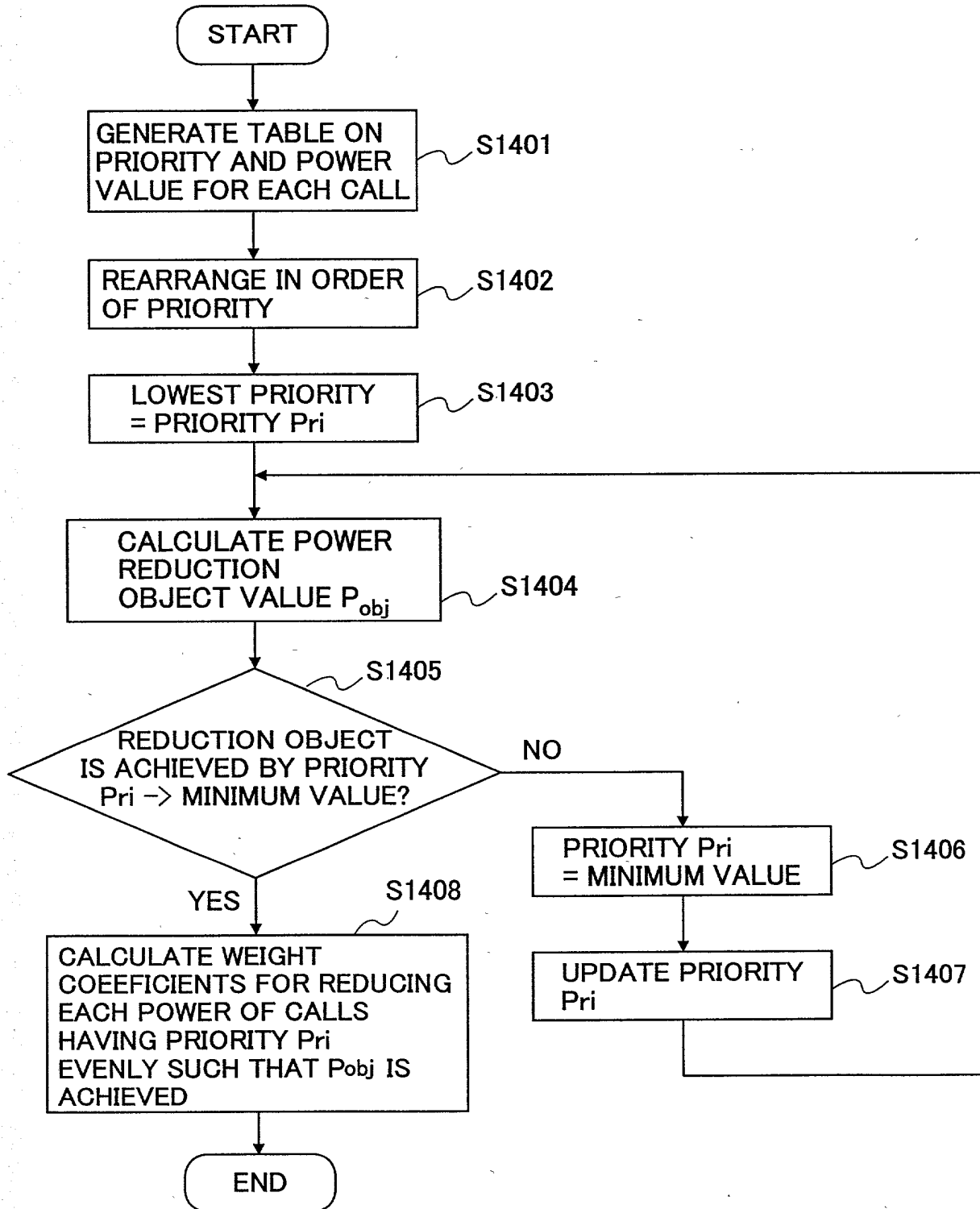


FIG.22

